

PTO/SB/08B (08-03)
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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	10/644,432
		Filing Date	08/19/2003
		First Named Inventor	Robert A. Dunstan
		Art Unit	Not Yet Assigned
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	110349-133958
Sheet	1	of	1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
J	1	"Advanced Configuration and Power Interface Specification," Compaq Computer Corporation, et al., Revision 2.0c, October 30, 2003, pp. ii-562	

Examiner Signature	<i>[Signature]</i>	Date Considered	3/9/2006
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2
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First Named Inventor	Dunstan et al.
Art Unit	not yet known
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
		Country Code ² -Number ⁴ -Kind Code ³ (if known)	MM-DD-YYYY		
<i>[Signature]</i>	3	EP 0 805 386 A1	11/5/97	IBM	
	4	CN 1402106	3/12/03	Wang	

**Examiner
Signature**

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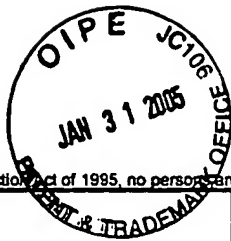
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 2 of 2

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	5	Advanced Power Management, BIOS Interface Specification, Revision 1.2, Feb 96, Intel Corporation and Microsoft Corporation	

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